

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	5759496
<b>Application Number:</b>	10829495
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	6926
<b>Title of Invention:</b>	Method and system for fail-safe renaming of logical circuit identifiers for rerouted logical circuits in a data network
<b>First Named Inventor/Applicant Name:</b>	William Taylor
<b>Customer Number:</b>	83417
<b>Filer:</b>	Felipe Hernandez/Kathryn Bartelt
<b>Filer Authorized By:</b>	Felipe Hernandez
<b>Attorney Docket Number:</b>	20103/030290
<b>Receipt Date:</b>	23-JUL-2009
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<b>Application Type:</b>	Utility under 35 USC 111(a)

### Payment information:

Submitted with Payment	yes
Payment Type	Electronic Funds Transfer
Payment was successfully received in RAM	\$940
RAM confirmation Number	3536
Deposit Account	
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Request for Continued Examination (RCE)	030290_RCE_72309.pdf	78687 9e57616ae4c940192b2b1eb5cac76300ff5a6b	no	3
<b>Warnings:</b>					
This is not a USPTO supplied RCE SB30 form.					
<b>Information:</b>					
2	Amendment After Final	030290_FOA_Response_72309.pdf	134279 00b36d514d534804308bceef545dc6781d041a7a	no	10
<b>Warnings:</b>					
<b>Information:</b>					
3	Information Disclosure Statement (IDS) Filed (SB/08)	030290_IDS_72309.pdf	87762 bc5e1bc13a9484caddf02a2e462a0479d72a4c	no	4
<b>Warnings:</b>					
<b>Information:</b>					
This is not an USPTO supplied IDS fillable form					
4	Fee Worksheet (PTO-875)	fee-info.pdf	31904 4a1358f7d6de20e5dc1104ff2178239702d9e711	no	2
<b>Warnings:</b>					
<b>Information:</b>					
Total Files Size (in bytes):			332632		
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